

FIG. 1

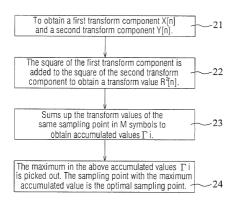


FIG. 2

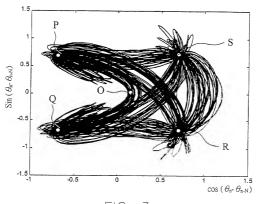


FIG. 3

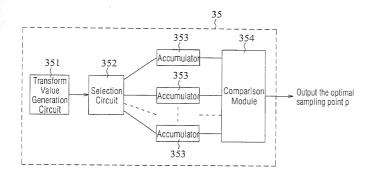


FIG. 4

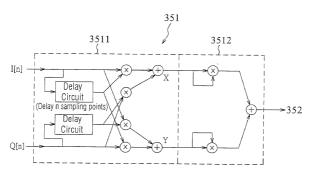
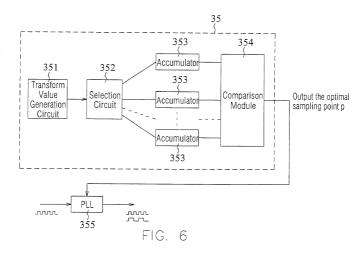
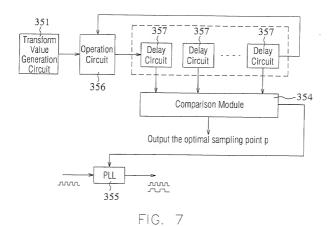


FIG. 5





31 32 33 35

RF Circuit A/D Matched Symbol Timing Recovery Circuit

FIG. 8